Connect JLINK tool and radar according to the following connection method, and connect JLINK to the computer

JLINK	雷达
VCC	3V3
GND	GND
SWDIO	SD
SWCLK	SC





## 1: Create a new project.

	J. S	EGGEF	R J-Flash	n V6.40			-		×
	File	Edit	View	Target	Options	Window	Help		
						1	Welcome to J-Flash Please select one of the following start options: Copen recent project: Create a new project. Do not show this message again. 2 Start J-Flash		
e - e	Appl - J - J	OG icatio -Flash LinkAN	n log s V6.40 M. dll V	tarted (J-Flash 6.40 (DL	compiled ( L compiled	0ot 26 201: Oct 26 20	8 15:06:20) 18 15:06:02)		F <
	list of	f MCU	device	s read su	uccessfully	(6581 Dev	ices)		11.

2. Select the chip type: AT32F403ARGT7 under the AT category, then click OK

(If you cannot find the chip catalog, please contact us and we will provide you with a patch.)

SEGGER J-Flash V6.40	- 0	×
File Edit View Target Options Window Help Create New Project × Welc Project × View Cortex M0 Little endian		
C Target Interface Speed (kHz)		-
<u></u>		
e Relication log started - J-Flash V6.40 (J-Flash compiled Oct 26 2018 15:06:20) - JLinkABM. dll V6.40 (DLL compiled Oct 26 2018 15:06:02) e		
List of MCU devices read successfully (6581 Devices)		11.

			,	
Aanufacturer	Device	Core	Flash size	RAM size
rteryTek	AT32F403A UNIVERSAL TYPE2	Cortex-M4	1024 KB + 16 MB	224 KB
rtervTek	AT32F403A UNIVERSAL TYPE2	Cortex-M4	1024 KB + 16 MB	224 KB
rteryTek.	AT32F403ACCT7	Cortex-M4	256 KB	224 KB
rtervTek.	AT32F403ACCU7	Cortex-M4	256 KB	224 KB
rteruT ek	AT32E403ACET7	Cortex-M4	512 KB	224 KB
rteruTek	AT32E403ACEU7	Cortex-M4	512 KB	224 KB
rteruTek	AT32E403ACGT7	Cortex M4	1024 KB	224 KB
rteryTek rteryTek	AT32F403ACC117	Cortex M4	1024 KD	224 KD
rtery i ek	AT 32F403ACGU7	Contex-M4	1024 NB	224 NB
rtery i ek	A132F403AHC17	Lortex-M4	256 KB	224 KB
rtery I ek	AT 32F 4U3ARE 17	Cortex-M4	512 KB	224 KB
rteryTek.	AT32F403ARGT7	Cortex-M4	1024 KB	224 KB
rteryTek	AT32F403AUOD	Cortex-M4	48 Bytes	224 KB
rteryTek.	AT32F403AVCT7	Cortex-M4	256 KB	224 KB
rteryTek	AT32F403AVET7	Cortex-M4	512 KB	224 KB
rtervTek	AT32F403AVGT7	Cortex-M4	1024 KB	224 KB
rteruTek	AT32E403CBT6	Cortex-M4	128 KB	32 KB
rteruTek	AT32E403CCT6	Cortex-MA	256 KB	224 KB
rtoruTok	AT32F403CC10	Cortex M4	250 KD	224 KD
ItelyTek	AT32F403CC00	Collex-M4	230 KD	224 KD
пертек	AT 32F403CE16	Cortex-M4	512 KB	224 KB
rtery i ek	A132F403CE06	Lortex-M4	512 KB	224 KB
rteryTek	AT32F403CGT6	Cortex-M4	1024 KB	224 KB
rteryTek.	AT32F403CGU6	Cortex-M4	1024 KB	224 KB
rteryTek	AT32F403RCT6	Cortex-M4	256 KB	224 KB
rteryTek	AT32F403RET6	Cortex-M4	512 KB	224 KB
rtervTek	AT32F403BGT6	Cortex-M4	1024 KB	224 KB
rteruTek	AT32E403U0B	Cortex-M4	48 Butes	224 KB
SEGGER J-Flash Edit View	V6.44e Target Options Window Help		-	-
SEGGER J-Flash Edit View	V6.44e Target Options Window Help			-  -
SEGGER J-Flash Edit View	V6.44e Target Options Window Help Weld Plee C Target Dev AtteryTek Little endi SwD	Project ice AT32F403ARGT7 an _ fface Speed (kHz) fface 4000	× 2 1	-
Edit View Edit View	V6.44e Target Options Window Help	Project ice AT32F403ARGT7 an _ fface Speed (kHz)	×	

## 3: Drag the radar firmware to this area and you will be prompted to modify the address.The normal starting address is0X8000000.

SEGGER J-Fla	ash V6.44e - [C:\Users\86186'	Desktop\jlink\AT.jflash]				-	×
File Edit View	v Target Options Windo	v Help					
Project - AT Name Host connection Target interface Init SWD speed SWD speed MCU Core Endian Check core ID Use target RAM Flash memory Base address Flash size	Value         SX           USB [Device 0]         SXD           4000 kHz         4000 kHz           AtteryTek AT32F403A         Cortex-M4           Little         No           224 KB @ 0x20000000         Internal bank 0           0x8000000         1024 KB	Enter start address Start address (hex. 0x (00000)	OK Cancel	<			
Application log -J-Flash V6 4 - JLinkARM dll Opening project - Project open Failed to open Opening data fi	started 4e (J-Flash compiled Apr 5 V6 44e (DLL compiled Apr 4 file [C:\Users\08186\Deskto ed successfully data file [C:\Users\08186\Desktop\9	2019 16:16:51) 2019 16:15:03) \\link\NT.jflash] ktop\jlink\]. 颜固件\最新固件\AT固件\通用固件\103\genera	l_firmware_1	03\boot_202	10819. bin]		€3 ^ E. <
List of MCU devi	ces read successfully (7019 [	evices)					

## 4: Click Target -> Connect

le <u>E</u> dit <u>V</u> iew	v <u>I</u> arget <u>O</u> ptions <u>W</u>	îl <mark>(indow <u>H</u>elp</mark>																			_	L	1	×
Project - ne	w p 🗖 💌 🔀	C:\Users	861	86\D	eskto	op∖∎	达固	]件.he	ex															×
Name	Value	Address:	0x800	00000			x1	x2	×4															
Host connection	USB [Device 0]		0		0	0					0	0	•	n	0									
Target interface	SWD	Haaress	20	1	4	3	4	5	6		8	9	H	B	DO	D	E	F	HS	,				
Init SWD speed	4000 kHz	8000000	38	CH	00	20	63	EH	99	08	BI	Er	00	68	83	Er	00	68	8.	. 1.				
SWD speed	4000 kHz	8000010	82	E5	00	68	B.5	E5	NN	68	BA	E5	NN	08	NN	00	00	00						
		8000020	00	00	00	00	00	00	00	00	00	00	00	00	BB	E7	00	08	••					
MCU	ST STM32F401RC	8000030	BD	E7	00	08	00	00	00	00	BF	E7	00	08	C1	E7	00	08						
Lore	Lortex-M4	8000040	D5	EA	00	08	D9	EA	00	08	DD	EA	00	08	E1	EA	00	08						
Englan Chack core ID	Ves (0v4PA00477)	8000050	E5	EA	00	08	E9	EA	00	08	ED	EA	00	08	F1	EA	00	08						
Use target BAM	64 KB @ 0x2000000	8000060	F5	EA	RR	08	F9	EA	RR	08	FD	EA	RR	08	<b>Ø1</b>	EB	88	88						
and an government		8000070	95	EB	90	0.9	60	EB	00	69	ØD	EB	90	99	11	EB	90	69						
lash memory	Internal bank 0	0000070	45	ED	00	00	40	ED	90	00	40	ED	90	00	11	ED OO	00	00						
Base address	0x8000000	8000080	15	EB	00	08	19	EB	00	68	10	EB	00	08	00	00	00	00	•••	• • • •	• • • •			
Flash size	256 KB	8000090	00	00	00	00	00	00	00	00	00	00	00	00	21	EB	00	08				!		
		80000A0	C9	E7	00	08	25	EB	00	08	29	EB	00	08	2D	EB	00	08		% .	)		· · · ·	
		80000B0	31	EB	00	08	35	EB	00	08	39	EB	00	08	3D	EB	00	08	1.	5.	9			
		8000000	41	EB	ØЙ	<b>Ø</b> 8	45	EB	ØØ	08	49	EB	<b>A</b> A	08	4D	EB	<b>N</b> A	08	A.,	E.	I		1	
		8000000	51	ED	88	0.9	<b>D</b> 2	F7	00	69	55	ED	00	69	00	00	00	00	0					
		0000000	51	ED	00	00	ED	ED	00	00	55	ED	00	00	00	00	00	00	¥-					
		80000E0	57	EB	00	68	50	EB	00	68	61	EB	99	68	00	00	00	00	Υ.		a			
		SOUGHLO	00	NN	00	00	NN	NN	NN	00	00	00	NN	00	65	EB	00	68		• • • •			••••	
		8000100	00	00	00	00	69	EB	00	08	4F	E8	00	08	6D	EB	00	08	•••	i.	0		۹	
		8000110	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00						100
		00000100	EQ	E0	66	no	74	ED	66	190	63	EO	66	190	70	ED	66	190	U	~	~		•	-
106																					[	- 10		83
mlication log	r storted																							
<ul> <li>J-Flash V6.4</li> <li>JLinkARM dll eating new pr New project sening data fi</li> <li>Data file op</li> </ul>	(b) (T=flash compiled Oct V6.40 (DLL compiled Oc oject created successfully ened successfully (6044 ened successfully (6044	26 2018 15:06 t 26 2018 15:00 top\雷达固件.h. 1 bytes, 1 ran	:20) 6:02) ex] . ge, 0	) CRC o	f da	ta =	0x6C	04981	LF, C	RC o	£ £i]	Le =	0x35	1045	5E)									2 4
125							_	_	_	_	_		_	_	_	_	_	_	_	_				_

<ol><li>It will show connected successfully after successfully after connected successfully after connected successfully after connected successfully after successfully after connected successfully after connected successfully after connected successfully after successfully</li></ol>
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SEGGER J-FI	ash V6.40 - [new project	t*]																						×
<u>Eile E</u> dit <u>V</u> iev	w <u>T</u> arget <u>O</u> ptions <u>V</u>	<u>M</u> indow <u>H</u> elp																						
R Project - ne	ew p 🗖 🖻 🔀	C:\Users	861	86\D	eskte	op\a	达固	副件.h	ex													-		×
Name	Value	Address:	0x800	00000		_	x1	x2	x4															
Host connection	USB [Device 0]			1											1									_
<b>T</b>	01.0	Address	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	ASC	II				-
Target interface	5WD 4000 kH=	8000000	38	CA	00	20	69	EA	00	08	<b>B1</b>	E7	00	08	<b>B</b> 3	E7	00	08	8	i.			• • •	_
SWD speed	4000 kHz	8000010	B5	E7	00	08	B7	E7	00	08	<b>B9</b>	E7	00	08	00	00	00	00						
o no opecia	1000 1112	8000020	00	00	00	00	00	00	00	00	00	00	00	00	BB	E7	00	08						
MCU	ST STM32F401RC	8000030	BD	E7	00	08	00	00	00	00	BF	E7	00	08	C1	E7	00	08						
Core	Cortex-M4	8000040	D5	FA	ØØ	08	<b>D9</b>	FA	ØØ	08	DD	FA	ØØ	08	F1	FA	ØØ	08						
Endian	Little	8000010	FF	EO	00	69	EO	EO	00	69	ED	EO	00	69	E4	EO	00	69						
Check core ID	Yes (0x4BA00477)	0000050	ES	EH	90	00	E7	EH	90	00	ED	EH	90	00	r1	EH	90	00						
Use target HAM	64 KB @ 0x20000000	8000020	F5	EA	00	08	F9	EA	NN	08	FD	EA	NN	68	01	EB	00	68		•••	•••	••••	•••	
Elash memoru	Internal bank 0	8000070	05	EB	00	08	09	EB	00	08	ØD	EB	00	08	11	EB	00	08			• • •			
Base address	0x8000000	8000080	15	EB	00	08	19	EB	00	08	1D	EB	00	08	00	00	00	00						
Flash size	256 KB	8000090	00	00	00	00	00	00	00	00	00	00	00	00	21	EB	00	08				!		
		80000A0	C9	E7	00	08	25	EB	00	08	29	EB	00	08	2D	EB	00	08		.×.	>			
		8000080	31	FB	ØØ	08	3.	FB	ØØ	08	39	FB	ØØ	08	30	FB	00	80	1	5	9	=		
		9000000	41	ED	00	00	45	ED	90	00	40	ED	00	60	40	ED	00	60		E.				
		0000000		ED	00	00	45	ED	00	00	-17	ED	00	00	-10	ED	00	00	n	· E -				
		8000000	51	EB	00	68	D3	E7	99	68	55	EB	99	68	99	00	00	00	4					
		80000E0	59	EB	00	08	5D	EB	00	08	61	EB	00	08	00	00	00	00	¥	.1.	a		• • •	
		80000F0	00	00	00	00	00	00	00	00	00	00	00	00	65	EB	00	08				e		
		8000100	00	00	00	00	69	EB	00	08	4F	E8	00	08	6D	EB	00	08		.i.	0	m		
		8000110	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00						
		0000130	6.0	EO	66	66	71	ED	66	190	63	E0	66	190	76	ED	66	60	U	~	~	••		-
LOG		-																			C			23
- ROMTL1[0][0]	]: E000E000, CID: B105E	DOD, PID: OOOBB	DOC S	SCS-M	7																			1
- ROMTL1[0][1]	: E0001000, CID: B105E	DOD, PID: 003BB	002 I	TWC																				
- ROMIBILUJU2	]: £0002000, CID: B105£0 ]· £00000000, CID: B105£0	DOD, FID: 00288 DOD, FID: 00388		TM																				
- ROMTE1[0][4]	: E0040000, CID: B10590	DOD, PID: OOOBB	PA1 7	PIU																				
- ROMTb1[0][5]	]: E0041000, CID: B10590	DOD, PID: OOOBB	925 I	TM																				
- Initialia	nit sequence zed successfully																							
- Target inter	face speed: 4000 kHz (1	Fixed)																						
- J-Link found	d 1 ITAG device. Core Il	D: 0x2BA01477 (	None)																					
connected St	docessionity																							
<																							2	۶.
			_	_	_	_	_	_	_	_	_	_	_			_	_	_		_			_	_
eady												C	onn	ecter	d	Co	e Id	: 0x2	<b>BA01</b>	477	Sp	eed: 4	4000	kH
U.	200023.004				_	_	-		_	_	_	-		-	-	-				10			1-	-

6: Click Target -> manual Programming -> Erase Chip to erase radar flash.

SEGGER J-Flash V6.40 - [new project *	1	- o x
Eile     Edit     View     Iarget     Options     W       Project - new p     Image: Comparison of the second	indow <u>H</u> elp 	
Host connection USB [Device 0]		
Target interface     SWD       Init SWD speed     4000 kHz       SWD speed     4000 kHz       MCU     ST STM32F401RC       Core     Coreex-M4       Endian     Litle       Check core ID     Yes (0x48A00477)       Use target RAM     64 KB @ 0x2000000       Flash memory     Internal bank 0       Base address     256 KB	Address         0         1         2         3         4         5         6         7         8         9           8000000         38         CA         00         20         69         EA         00         08         15         E7         00         08         PT         00         08         00 </th <th>A         B         C         D         E         F         ASCII           7         00         08         B3         E7         00         08         08         1         1           7         00         08         00         00         00         00         1</th>	A         B         C         D         E         F         ASCII           7         00         08         B3         E7         00         08         08         1         1           7         00         08         00         00         00         00         1
	8000000         41         EB         00         98         45         EB         00         88         49         EI           8000000         51         EB         00         08         35         F2         00         08         55         EI           8000000         50         EB         00         08         50         EB         00         08         55         EI           8000000         60         00 <th>B       00       08       4D       EB       00       08       AEIM         B       00       08       00       00       00       QU         B       00       08       00       00       00       QU         B       00       08       00       09       00       YI         0       00       05       EB       00       08          0       00       06       5       EB       00       08          0       00       00       EB       00       08      </th>	B       00       08       4D       EB       00       08       AEIM         B       00       08       00       00       00       QU         B       00       08       00       00       00       QU         B       00       08       00       09       00       YI         0       00       05       EB       00       08          0       00       06       5       EB       00       08          0       00       00       EB       00       08
Application log started - J-Flash VG.40 (J-Flash compiled Oct - JLinkAMM all VG.40 (DLL compiled Oct Creating new project - New project created successfully Opening data file [C:\Users\06186\Beskt - Data file opened successfully (80441 (80441)	26 2018 15:06:20) :26 2018 15:06:02) :op\雷达固件.hex] bytes, 1 range, CRC of data = 0x6CO49B1F, CRC of file :	= 0x351D455E)
Ready		

7: Click Target -> manual Programming ->Program & Verify to burn the new firmware and it will show Connected successfully after burning.

ile <u>E</u> dit <u>V</u> iev	ash V6.40 - [new project v <u>T</u> arge <mark>t Q</mark> ptions <u>W</u>	*] ( <mark>indow <u>H</u>elp</mark>															-			×
Name	w p   We let us a local control of the local con	Address:	3 <b>6186</b> x80000	\Deskte	op\雷)	达固件.h x1 <u>x2</u>	ex x4													x
Target interface Init SWD speed SWD speed Core Endian Check core ID Use target RAM Flash memory Base address Flash size	SWD 4000 kHz 4000 kHz ST STM32F401RC Cortex-M4 Little Yes (0x48A00477) 64 KB @ 0x20000000 Internal bank 0 0x4000000 256 KB	Address 8000000 8000010 8000010 8000020 8000050 8000050 8000050 8000050 8000050 8000050 8000050 8000050 8000050 8000050 8000050 8000050 8000050 8000050 800050 8000550 800550	0         ::           38         C           38         C           85         F           90         0           90         0           90         F           90         F           90         F           90         F           90         F           90         F           91         F           92         F           93         F           90         F <tr td=""></tr>	1         2           1         2           1         2           1         00	20 20 08 08 08 08 08 08 08 08 08 08 08 08 08	4         5           69         EA           B7         E7           00         00           00         00           00         00           D9         EA           F9         EA           00         00           25         EB           00         25           845         EB           D3         E7           5D         EB           00         00           69         EB           00         00           25         EB           03         E7           5D         EB           00         00           69         EB           00         00           69         EB           00         00           69         EB           01         E7	6 90 90 90 90 90 90 90 90 90 90 90 90 90	7         08         1           08         1         00         1           08         1         08         1           08         1         08         1           08         1         08         1           08         1         08         1           08         1         08         1           08         1         08         1           08         1         08         1           08         1         08         1           08         1         08         1           08         1         08         1           08         1         08         1           08         1         08         1           08         1         08         1           08         1         08         1           08         1         08         1           08         1         08         1           08         1         0         1           08         1         0         1           08         1         0         1           08	88         9           BB1         E           BB7         E           B00         Ø           BBF         E           B00         Ø           BBF         E           B00         Ø           BBF         E           BDD         Ø           BDD         <	A           A           Q	B           Ø8           Ø8	C B3 00 BB C1 E1 F1 01 11 21 20 3D 4D 00 65 6D 00	D           E7           00           E7           EA           EA           EB           00           EB           EB	E 00 00 00 00 00 00 00 00 00 00 00 00 00	F 08 00 08 08 08 08 08 08 08 08	ASCII 8i 		9   		•
ROMTALOS ROMTALOS ROMTALOS ROMTALOS ROMTALOS ROMTALOS ROMTALOS ROMTALOS - ROMTALOS - ROMTALOS	: E000E000, CID: B105E0 : E0002000, CID: B105E0 : E0002000, CID: B105E0 : E0004000, CID: B105E0 : E0044000, CID: B105E0 : E0041000, CID: B105E0 it sequence : Gal successfully face speed: 4000 kHz (F 1 THG dayie, Core ID sccessfully	00, FID: 000BB0 00, FID: 002BB0 00, FID: 002BB0 00, FID: 002BB0 00, FID: 000BB9 00, FID: 000BB9 00, FID: 000BB9 01, FID: 000BB9 01, FID: 000BB9 01, FID: 000BB9 01, FID: 000BB9 01, FID: 000BB9 01, FID: 000BB9	OC SCS D2 DWT D3 FPB D1 ITM A1 TPI 25 ETM one)	:- <b>1</b> 17 L U L						0 1414		.,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,			140	0~~				23
dy										Conn	ected	1	Core	e Id:	0x2	BA0147	7 Sp	eed:	4000	kH